

<b>Form PTO-1449</b>  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>				Docket Number (Optional) A5GN2420US		Application Number <u>101665827</u>
				Applicant Patterson, Gregory Wayne		
				Filing Date		Group Art Unit
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5 2 3 1 3 9 9	7/27/93	Gorman, et al.	341	159	
	5 5 8 9 8 3 1	12/31/96	Knee	341	159	
	5 7 0 6 0 0 8	1/6/98	Huntley, et al.	341	156	
	5 9 9 0 8 1 4	11/23/99	Cromam, et al.	341	118	
	6 0 1 4 0 9 7	1/11/00	Brandt	341	156	
	6 4 5 2 5 1 9	9/17/02	Swanson	341	120	
<b>FOREIGN PATENT DOCUMENTS</b>						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES NO
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
	Pettschacher, Reinhard, et al, "A 10b, 75MSPS Subranging A/D Converter with Integrated Sample and Hold", IEEE Journal of Solid-State Physics, Vol. 25, No. 6, December, 1990					
	Waltari, Mikko, et al, "reference Voltage Drive for Low Voltage CMOS A/D Converters", IEEE International Conference on Electronics, Circuits and Systems, Vol. 1, No. 6, 2001					
EXAMINER				DATE CONSIDERED <u>07/27/2004</u>		
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP§609; Draw line through						